

COEN 171 Spring 2009
Project #3: Assessment Form

Deliverables Checklist

Item	Yes (on time)	Yes (late)	No
Report			
Files (electronic)			

Evaluation

Item	Detail	Value (1 – 10)	Weight	Score
Report	Includes required sections		0.5	0
	Professional appearance		0.5	0
	Spelling/grammar/clarity		0.5	0
	Summary information		0.5	0
Design	R-Type design quality		1	0
	I-Type design quality		1	0
	J-Type design quality		1	0
Functionality	R-Type instruction works		1	0
	I-Type instruction works		1	0
	J-Type instruction works		1	0
Test & Verification	Testing processes described		0.5	0
	Testing processes sufficient		0.5	0
Performance	Simulation timing numbers		0.5	0
	Critical path design analysis		0.5	0
Total				0

Bonuses

Item	Detail	Value	Done	Score
Simulation	Short sequence	5	0	0
	Project #1 Program	10	0	0
Architecture	Multi-cycle datapath	10	0	0
	Pipelining	30	0	0
Instruction	Additional	5	0	0
	Additional	5	0	0
	Additional	5	0	0
	Additional	5	0	0
	Additional	5	0	0
	Performance	5	0	0
Synthesis	Xilinx FPGA	5	0	0
Total Bonuses				0

Summary

Item	Score
Evaluation	0
Bonuses	0
Total Team Score	0

Person					
Peer Review					
Final Grade	0	0	0	0	0